REV-01 BSP/01/05

## **B.Sc. PHYSICS** THIRD SEMESTER DIGITAL SYSTEMS & APPLICATIONS B SP = 303 OLD COURSE [REPEAT] [USE OMR FOR OBJECTIVE PART]

SET

2023/12

Full Marks: 70

Duration: 3 hrs.

Time: 30 min.

(Objective)

Marks: 20

 $Choose\ the\ correct\ answer\ from\ the\ following:$ 

1X20 = 20

1. The binary addition 1+1+1 gives b. 10 a. 111 d. 11 c. 110 2. The number  $1000_2$  is equivalent to decimal number b. Eight a. One thousand d. Sixteen c. Four 3. The only function of a NOT gate is to a. Stop a signal b. Re-complement a signal d. Act as a universal gate c. Invert an input signal 4. A NOR gate is ON only when all its inputs are b. Positive a. ON d. OFF c. High 5. An S-R latch is a

b. Synchronous circuit a. Combinational circuit c. One bit memory element

d. One clock delay element

6. In a sequential circuit the output state depends upon

a. Past output states and present input b. Input states only states

c. Input and output states

d. None of these

7. According to the algebra of logic,  $(A + \overline{A})$  equals

b. 1 a. A

d. AA c. ()

8. The expression ABC can be simplified to

b. AB+ BC+CA d. A . B . C d.  $\tilde{A} + \tilde{B} + \tilde{C}$ C. AB+C

9. D-Flip flop can be configured from a

a. J-K Flip flop and an inverter b. R-S Flip flop d. J-K Flip flop c. R-S Flip flop and an 'nverter

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<ul><li>10. Boolean algebra is essentially based on a. Symbols</li><li>c. Truths</li></ul>	b. Logic d. Numbers
<ol> <li>The frequency of oscillation of an astable ma. Value of collector load resistor</li> <li>Value of transistor β</li> </ol>	nulti-vibrator depends mainly on the b. RC value of the circuit d. Width of the input pulse
<ol> <li>First integrated circuit chip was developed</li> <li>a. C.V. Raman</li> <li>J.S. Kilby</li> </ol>	by b. W.H. Brattain d. Robert Noyce
13. An integrated circuit is  a. A complicated circuit c. Much costlier than a single transistor	<ul> <li>b. An integrated device</li> <li>d. Fabricated or a tiny silicon chip</li> </ul>
<ol> <li>Most important advantage of an IC is its</li> <li>Easy replacement in case of circuit failure</li> <li>Reduced cost</li> </ol>	b. Extremely high reliability d. Low power consumption
<ul> <li>15. A microprocessor is ALU</li> <li>a. And control unit on a single chip</li> <li>c. Register unit and I/O device on a single chip</li> </ul>	b. And memory on a single claip d. Register unit and control unit on a single chip
<ul><li>16. The signal to be viewed on the screen of an a. Across its X-Plate</li><li>c. To the horizontal amplifier</li></ul>	oscilloscope is apolied b. Across its Y- Plate d. To the triggered circuit
17. The digital system usually operates on a. Binary c. Octal	system. b. Decimal d. Hexadecimal
<ol> <li>A logic gate is an electronic circuit which</li> <li>Makes logic decision</li> </ol>	b. Allows electron flow only in one direction
<ul><li>c. Works on binary algebra</li><li>19. According to the Absorptive Laws of Boole</li></ul>	d. Alternates between 0 and 1 values
a. A c. AB	b. B d. A • A
20. The time period of IC-555 mono-stable mul a. 1,38 RC c. 1,45 RC	tivibrator operation is b. 1.1 RC d. 13.8 RC

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## **Descriptive**

Time: 2 hrs. 30 mia. Marks: 50

## [Answer question no.1 & any four (4) from the rest ]

What is the advantage of J-K Flip flop over S-R Flip flop? How is

- =10 an S-R Flip flop converted to J-K Flip flop? Write the truth table for J-K flip flop and convert it to characteristics table and excitation table. a. What is a binary coded decimal? Write the following numbers 4+3+3 in BCD code: 157, 2498 and 673. b. Write the steps involved in 2's complement subtraction. Use 2's complement to subtract 10102 from 11012. c. Find the decimal equivalent of the following binary numbers: 101, 1001 and 10.011. a. Draw the equivalent diode relay circuit of an AND gate and 4+4+2 =10 explain the operation. b. Sketch the symbol and construct the truth table for XOR and NAND gates. c. Draw a logic circuit for the Boolean expression  $y = \tilde{A} \cdot B \cdot C + (A + B) \cdot \tilde{C}$ a. Using the tath table, prove that A + AB = A + B and illustrate 4+2+4
  - b. Simplify the Boolean expression ABC + ABC + ABC + ABC + ABC
    - c. Prove the Boolean identity (A+B)(A+C)=A+BC

the equivalence with the help of a switching circuit.

2+4+4

6+4=10 a. Draw the block diagram, write the truth table for a hexadecimal to binary Encoder and implement it using logic gate. b. Construct a 8×1 MUX using a 4×1 MUX. 8+2=10a. With the help of a neat and labeled diagram explain the working of an astable multi-vibrator. b. The components of IC-555 Timer astable multi-vibrator is given as: RA=7.5 k $\Omega$ , RB= 7.5 k $\Omega$ , C= 0.1 $\mu$ F, VCC= 5 V. Determine the frequency. 5+5=10 7. a. Mention the advantages of ICs. b. Differentiate between Linear ICs and Digital ICs. Draw the block diagram of the 8085A Microprocessor. Explain its 5+5=10

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organization and operation.