REV-01 BPT/11/16

> BACHELOR OF PHYSIOTHERAPY FIRST SEMESTER MECA & BASIC ELECTROTHERAPY

BPT - 103 [REPEAT] ILSE OMR SHEET FOR OBJECTIVE PART]

Duration: 3 hrs.

Objective )

Time: 30 min.

Full Marks: 70

Marks: 20

2023/12

SET

A

1×20=20

Choose the correct answer from the following:

- 1. According to KCL,
  - a. Σ I=1
  - c. Σ I=0

- b. Σ I=2
- d. I=0

- 2. KVL can be applied to
  - a. Closed path
  - c. Both (a) & (b)
- 3. Transistor is a
  - a. Three junction device
    - c. Uni-junction device

- d. None of these
- b. Two junction deviced. All of these

b. Open path

- 4. In case of conductor, the forbidden energy band is
  - a. Large

b. Very large

c. Small

d. Negligible

- 5. CPU consists of
  - a. ALU & Memory

- b. ALU & Control Unit
- c. Control Unit & Memory
- d. All the above
- 6. How many NAND gates are required to construct an AND gate?
  - a. 3

b. 2

c. 4

- d. 1
- 7. In 4:1 MUX, the number of select line is
  - a. 2

b. 3

c. 1

- d. 4
- 8. Voltage division rule is applicable to
  - a. Series Circuit

b. Parallel Circuit

c. Both (a) & (b)

d. All the above

- 9. DEMUX is also called
  - a. Data selector
  - c. Data analyzer

- b. Data distributor
- d. All the above

wire having length 2m and area 4m² has a resistance of 8 Ohm. Its resistivity is 8 ohm-m
20 ohm-m d. 4 ohm-m
nary equivalent of the number (25) <sub>10</sub> is 10101  b. 11011 11001 d. 01111
de is a Three terminal device One terminal device  d. None of these
e 2's compliment of the number 10101101 is 01010010 b. 11010010
01010010 b. 11010010 01010011 d. 11101111
e hexadecimal form of the binary number 11111011 is
A3 b. AF d. FB
nich of the following is responsible for arithmetic and logical operations?
ALU b. Memory
Control Unit d. All the above
onitor is an example of
Input Device b. Storage device
Memory device d. Output device
wo voltages of values 18 V and 12 V are connected in series opposing circuit, the ne tage will be
18 V b. 12 V
30 V d. 6 V
l Adder performs binary addition operation of
3 bits b. 2 bits
1 bits d. 7 bits
electronic circuit that converts AC to DC is called Converter b. Amplifier
e main feature of 2 <sup>nd</sup> generation of computer is
Transistor b. Vacuum tube
IC d. AI

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USTM/COE/R-01

## ( <u>Descriptive</u> )

Time: 2 hrs. 30 min. Marks: 50

## [Answer question no.1 & any four (4) from the rest]

1.	Discuss the features, advantages and disadvantages of generation of computer.	10
2.	Define Kirchhoff's laws. Two resistors of values $14\Omega$ and $10\Omega$ are connected across a voltage source of 6V. Another $10\Omega$ resistor and 5V voltage source are connected across the previous combination. Find the values of current flowing in the two mesh formed.	10
3.	<ul><li>a. Explain with the help of block diagram the architecture of computer.</li><li>b. Design a half-subtractor with the help of truth table.</li></ul>	5+5=10
4.	<ul> <li>a. Design a Full Adder circuit with the help of truth table.</li> <li>b. Subtract (15)<sub>10</sub> from (10)<sub>10</sub> in 2's compliment method.</li> </ul>	7+3=10
5.	What do you mean by Multiplexer? Design a 4:1 MUX with the help of truth table.	10
6.	<ul><li>a. Explain with the help of diagram 1×2 DEMUX.</li><li>b. Differentiate between Semiconductor, Conductor and Insulator.</li></ul>	5+5=10
7.	<ul> <li>a. What is a transistor? What are the various configurations of transistors? Explain with the help of diagram.</li> <li>b. In a circuit, if series opposing voltages are 12 V and 6 V and two resistors of values 4Ω &amp; 8Ω are connected in series, then compute <ul> <li>(i) Circuit current</li> <li>(ii) Power supplied by the two batteries</li> <li>(iii) Power discipated in two resistors</li> </ul> </li> </ul>	6+4=10

8. a. Find the binary equivalent of the decimal number (13.25)<sub>10</sub>.
b. Convert (11011.1011)<sub>2</sub> into decimal. 2.5+2.5

+5=10

c. Write short notes on half wave rectifier.