REV-00 BSE/06/10

> B.Sc. Electronics Second Semester Semi Conductor Devices and Circuits (BSE- 08)

Duration: 3Hrs.

Full Marks: 70

PART A (Objective) =20 PART-B (Descriptive)=50

PART-B (Descriptive)

Duration: 2 hrs. 40 mins.

I. Answer the following (any five)

- a) Explain the current components of transistor for common base configuration. Write the generalized transistor equation.
- b) Derive the expression for electrostatic potential difference at thermal equilibrium for P-N junction.
- c) Explain the construction and operation of Silicon Controlled Rectifier (SCR).
- d) Describe the operation of Unijunction Transistor (UJT).
- e) What is FET? Explain the theory of operation of FET.
- f) Explain the JFET drain characteristics with $V_{GS} = 0$.
- g) Explain how Wafers are prepared in IC fabrication.

II. Answer the following (any five)

a)Show the energy band diagram of extrinsic semiconductor at thermal equilibrium.

- b) Define average value, r.m.s. value, efficiency, ripples factor and peak inverse value of a rectifier.
- c) Explain the process of diffusion in IC fabrication.
- d) Write short notes on effective mass of an electron.
- e) What is break down device? Write the different applications of UJT.
- f) Find the value for ripple factor for half wave rectifier.
- g) What is MOSFET? Draw the construction of different MOSFETs.

5×5=25

Marks: 50

5×3=15

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III. Answer the following (any five)

- a) Write down the basic fabrication steps involved in IC fabrication.
- b) Which material is used for interconnection of IC? Explain the reason.
- c) Explain in brief operation and construction of DEMOSFET.
- d) Draw the two transistor model of SCR and its characteristic curve.
- e) Draw the DC and AC equivalent circuit of common base configuration of transistor.

- f) What are the applications of SCR?
- g) Define mobility and drift velocity.

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Marks - 20

Total Marks: 20

 $1 \times 20 = 20$

B.Sc. Electronics

Second Semester

Semi Conductor Devices and Circuits

(BSE-08)

Duration: 20 minutes

PART A (Objective)

Time: 20 mins

I. Choose the correct answer:

1. Semiconductor materials have bonds.a) Ionicb) Covalentc) Mutuald) Metallic

2. When a P-N junction is formed, diffusion current causes

a) Mixing of current carriers	b) Forward bias
c) Reverse bias	d) Barrier potential

3. The area within a semiconductor diode where no mobile current exist when it is formed is called regions

- a) Depletion
- b) Saturation
- c) Potential barrier d) Space charge
- 4. Junction breakdown occurs
 - a) Under high temperature condition
 - b) With forward bias
 - c) Under reverse bias condition
 - d) Because of manufacturing defects.

5. The ripple factor of a power supply is given by (symbol have the usual meaning)

a)
$$\frac{P_{dc}}{P_{ac}}$$
 b) $\sqrt{\frac{I_{rms}}{I_{dc}}} - 1$ c) $\sqrt{\frac{I_{dc}}{I_{rms}}} - 1$ d) $\frac{I_{dc}}{I_{rms}}$

6. The primary function of a rectifier filter is to

- a) Minimise ac input variation
- b) Suppress odd harmonics in the rectifier output
- c) Stabilise dc level of the output voltage
- d) Remove ripples from the rectified output

7. In the case of bipolar transistor, α is b) Positive and (1 a) Positive and $\rangle 1$ c) Negative and $\rangle 1$ d) Negative and $\langle 1 \rangle$ 8. Which of the following approximations is often used in electronic circuits? a) $I_C \cong I_E$ b) $I_B \cong I_C$ c) $I_B \cong I_E$ d) $I_E \cong I_B + I_C$ 9. FET consists of a a) Source b) Drain c) Gate d) All of the above). In JFET, the drain current is maximum when V_{GS} is d) equal to V_P b) negative c) positive a) zero 10. A JFET can be cut- off with the help of b) *V*_{DS} c) V_{DG} a) V_{GS} d) V_{DD} 11. The transconductance g_m of a JFET i8s equal to a) $-\frac{2I_{DSC}}{V_p}$ b) $\frac{2}{|V_P|} \sqrt{I_{DSS}I}_D$ c) $-\frac{2I_{DSS}}{V_p}\left(1-\frac{V_{GS}}{V_p}\right)$ d) $\frac{I_{DSS}}{V_{p}} \left(1 - \frac{V_{GS}}{V_{P}} \right)$ 12. Which semiconductor device acts like a diode and two resistors? a) SCR b) Triac c) Diac d) UJT 13. SCR turns OFF from conducting state to blocking state on a) Reducing gate current b) Reversing gate voltage c) Reducing anode current below holding current d) Applying ac to the gate

14. An integrated electronic circuit is

- a) A complicated circuit
- b) An integrated device
- c) Much costlier than a single transistor
- d) Fabricated on a tiny silicon chip

15. In monolithic ICs, all components are fabricated by.....

- a) Evaporation
- b) Sputtering
- c) Diffusion
- d) Oxidisation

16. In an integrated circuit, the SiO₂ layer provides

- a) Electrical connection to the external circuits
- b) Physical strength
- c) Isolation
- d) Conducting path

17. The foundation on which an IC is built is called an

- a) Insulator
- b) Base
- c) Wafer
- d) Plate

18. The minimum value of current required to maintain conduction in an SCR is called its.....

a) Commutation

b) Holding

- c) Gate trigger
- d) Break over

19. The main factor which differentiates DEMOSFET from an E- only MOSFET is the absence of

a) Insulated gate

b) Electrons

c) Channel

d) P-N junctions.

20. In a properly connected BJT, an increase in base current causes increase in

a) I_C only

- b) I_E only
- c) Both I_C and I_E

d) Leakage current
